



INFORMATION DISCLOSURE STATEMENT

Applicant : Miranda, et al.
 App. No. : 09/760,129
 Filed : January 12, 2001
 For : SYSTEM AND METHOD FOR
 OPTIMIZING SOURCE CODE
 Examiner : Andre R. Fowlkes
 Group Art Unit : 2122

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MAY 11 2004

Technology Center 2100

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing 1 reference that is also enclosed.

This Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(c)(2) before the mailing date of a final action and before the mailing of a Notice of Allowance. This Statement is accompanied by the fees set forth in 37 C.F.R. § 1.17(p). The Commissioner is hereby authorized to charge any additional fees which may be required or to credit any overpayment to Account No. 11-1410.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

By: 

Eric M. Nelson
 Registration No. 43,829
 Attorney of Record
 Customer No. 20,995
 (619) 235-8550

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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE MAY 06 2004 U.S. Patent and Trademark Office (USE SEVERAL SHEETS IF NECESSARY)	ATTY. DOCKET NO. IMEC193.001AUS	APPLICATION NO. 09/760,129
	APPLICANT Miranda, et al.	
	FILING DATE January 12, 2001	GROUP 2122

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
	Sumit Gupta, Miguel Miranda, Francky Catthoor, Rajesh Gupta, "Analysis of high-level address code transformations for programmable processors", Proceedings of the conference on Design, automation and test in Europe, P. 9-13, March 27-20, 2000, Paris, France.

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EXAMINER	DATE CONSIDERED
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